Job Offer

Job Title Senior Mixed-Signal ASIC project manager

- Contract type permanent
- Starting Date immediately
- Location Paris, Caen, Grenoble FRANCE
- Job function Lead the design planning and execution of mixed-signal ASIC across all facets of design from concept and new IP development to final silicon. Represent the collective engineering team in interfacing with management, product management/marketing, and key customers. Champion the development & deployment of best practices to improve program performance, efficiency, and produce repeatable results across a growing business.

Work description

- Drive accountability and awareness, solve problems, remove roadblocks, and proactively search out and address risks or weaknesses to ensure the project meets quality standards and completes on time and in budget
- Provide project leadership for mixed-signal ASIC by working with cross functional silicon development teams to meet internal and customer commitments
- Responsible for end-to-end project planning & rigorous execution management to improve project execution certainty
- Manage the relationship with the supply-chain partners such as foundry, testing house, packaging house and semiconductors IP providers.
- Represent the collective engineering team in interfacing with Product Marketing or the customer
- Enable communication & visibility to ensure alignment at all levels of the organization
- Development & deployment of best practices to improve program performance, efficiency, and produce repeatable results
- Champion lessons learned to drive continuous improvement & avoid known problems.
 Manage change into standard best practices.

Qualification and Experience

 You have a MSc. in Electrical engineering or equivalent and 5+ years of project or program management experience managing complex integrated circuit development projects



- You have a proven track record in the design and characterization of analog and mixed-signal functions and/or RF blocks
- You have a good vision of the analog and digital IC design flow
- Technical Program Manager with experience in management of design for mixedsignal IC products in a tight schedule
- Must possess strong leadership and communication skills to work with cross functional silicon development teams to meet internal and customer commitments.
- Familiar with project/program management tools such as MS Project
- Experience with product development cycles from concept to production
- Project governance; rollout of framework and process to manage and monitor project execution through entire development life cycle (PLC)
- Configuration & change management; rollout of methodology and process to monitor and control change requests (bug, enhancements)
- Risk management; rollout of methodology and process to manage technical & project risks
- Requirements management; rollout of methodology and process to elaborate products requirements
- Documentation management; rollout of methodology and processes to manage product specifications
- Problem solving and quality management: rollout Quality Assurance tools and methods
- Ideally, experience in leading project through ISO 9001audit process

How to apply

If you have a passion for IC design from specification to product release and the ambition to create something different, it will be a great pleasure to receive your resume at **jobs@scalinx.com**



About SCALINX

SCALINX is a fabless semiconductor company designing state-of-the-art Analog, Mixed-Signal & RF Integrated Circuits and Intellectual Property blocks for the Space/Defense, Industrial and Communications markets. Our core business is to provide tailored solutions to OEMs and semiconductor companies developing high-end circuits and systems with ultra-low power requirements and reduced Bill of Material.

Our expertise spans from the sensor interface to the digital interface. SCALINX's IC design team has a cumulated expertise of more than 250 years in the semiconductor industry with a proven track record of first-time-right tape-outs that led to many successful business stories.

To learn more about SCALINX, visit our web site www.scalinx.com

